

LOW RISE/FALL SKEWED INPUT BUFFER COMPENSATING PROCESS VARIATION

ABSTRACT OF THE DISCLOSURE

Buffer circuits and techniques that reduce skew between rising and falling times of output data as process conditions vary are provided. One or more process-dependent current sources may be utilized to compensate for process variations by supplementing the current drive of transistors used to precharge (PMOS) or discharge (NMOS) an output node of a secondary (e.g., inverter) stage of the buffer circuit.